

09/891,339 Updated S

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	74	surveillance with (test\$4 or diagnos\$4 or analys\$4 or analis\$4) with (plural\$4 or multipl\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:21
L2	124550	(determin\$4 or identif\$4 or clarif\$4 or confirm\$4) same time same (expir\$4 or elaps\$4 or abandon\$4 or interrupt\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:25
L3	13926	updat\$4 same respond\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:27
L4	6969	status adj2 request\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:28
L5	16920	(return\$4 or respond\$4 or forward\$4) near3 status	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:31
L6	0	1 same 2 same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:31
L7	0	1 and 2 and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:32
L8	0	1 and 2 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:32

L9	1	1 and 2 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:33
L10	0	1 same 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:33
L12	7	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:35
L13	2	1 and ("714"/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:36
L14	2	1 and (71\$/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:37
L15	23	1 and (7\$/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:56
L16	9	1 and ("340"/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:57
L17	47	1 not (15 or 16)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/26 13:58

Updated 5-09/89, 339  
NPL. Abstracts

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Set	Items	Description
S1	507899	PROCESS?R? ? OR CPU OR CPUS OR MICROPROCESS? OR CENTRAL()P-ROCESS??? ? OR MPU OR MPUS OR LOGIC(1W)CHIP? ?
S2	18255	S1(3N)(SURVEILL? OR SURVEY? OR MONITOR? OR CHECK??? ? OR TEST OR TESTS OR TESTED OR TESTING? ? OR DIAGNOS? OR DX OR DETECT???? ? OR ASSESS?)
S3	10254	S1(3N)(AUDIT OR AUDITS OR AUDITED OR AUDITING OR PROBE? ? - OR PROBING OR ANALYS? OR ANALYT? OR ANALYZ? OR SELFDIAGNOS? OR SELFTEST? OR SELFCHK?)
S4	7543	S1(3N)(AUTODIAGNOS? OR AUTOTEST? OR AUTOCHECK? OR BIST OR - EXAMIN? OR EVALUAT? OR INSPECT? OR TRACK? OR TROUBLESHOOT? OR TROUBLE()SHOOT?)
S5	568683	PARTITION? OR OS OR OSS OR OPERATING(1W)SYSTEM? ? OR SYSTEM? ?(1W)(SOFTWARE? OR SOFT()WARE? ?)
S6	6375	(MULTIPLE OR MULTIPLES OR PLURALIT? OR MANY OR MULTI OR SEVERAL OR NUMEROUS OR ADDITIONAL OR DIFFERENT OR MULTITUD? OR - SECOND)(1W)S5
S7	9794	(BOTH OR BETWEEN OR VARIOUS OR VARIETY OR GROUP? ? OR NUMBER? ? OR PAIR? ? OR ANOTHER OR TWO OR THREE OR COUPLE)(1W)S5
S8	633018	MEMORY? OR MEMORIES OR NVRAM?
S9	809	(NONVOLATILE OR NON()VOLATILE OR NV OR FLASH OR CORE OR BUBBLE OR MAGNET?? ? OR NONERAS? OR NON()(ERASE? OR ERASA?) OR - PERMANENT)(1W)(RAM OR RAMS)
S10	1008633	NVM? ? OR E2PROM? OR EEPROM? OR EAROM? OR EPROM? ? OR EAPROM? OR ROM OR ROMS OR PROM OR PROMS OR FPROM? ? OR NVS OR BOOTPROM? OR IDPROM? OR STORAGE?
S11	12721	S8:S10(3N)(PARTITION? OR DIVID? OR DIVISION? OR PORTION? OR REGION? OR SECTOR? ? OR ZONE? ? OR SECTION? OR SUBDIVID? OR - SUBDIVIS? OR SLICE?)
S12	24943	S8:S10(3N)(BLOCK? ? OR PART? ? OR PIECE? ? OR SUBSET? OR SUB()SET? ? OR SEGMENT? ? OR AREA? ?)
S13	53531	S8:S10(3N)(SURVEILL? OR SURVEY? OR MONITOR? OR CHECK??? ? - OR TEST OR TESTS OR TESTED OR TESTING? ? OR DIAGNOS? OR DX OR DETECT???? ? OR ASSESS?)

S14 29694 S8:S10(3N) (AUDIT OR AUDITS OR AUDITED OR AUDITING OR PROBE?  
? OR PROBING OR ANALYS? OR ANALYT? OR ANALYZ? OR SELFDIAGNOS?  
OR SELFTEST? OR SELFCHK?)

S15 22392 S8:S10(3N) (AUTODIAGNOS? OR AUTOTEST? OR AUTOCHK? OR BIST  
OR EXAMIN? OR EVALUAT? OR INSPECT? OR TRACK? OR TROUBLESHOOT?  
OR TROUBLE()SHOOT?)

S16 679182 STATUS?

S17 9190742 TIME OR TIMES OR MINUTE? ? OR PERIOD? ? OR HOUR? ? OR TIME-  
FRAME? OR TEMPORAL OR INTERVAL? ? OR FRAME OR FRAMES OR DURAT-  
ION?

S18 208358 S17(3N) (ELAPS? OR OVERDUE? OR DUE OR EXPIR? OR SPAN OR SPA-  
NS OR PASS?? ? OR PASSING OR PASSAGE? OR LAPS??? ? OR PAST OR  
CYCLE? ?)

S19 3604 S11:S12 AND S13:S15

S20 87 S19 AND S16

S21 1985 S19 AND (CONDITION? ? OR MODE OR MODES OR STATE OR STATES -  
OR CIRCUMSTANCE? OR SITUATION? ? OR PERFORM? OR BEHAVIOR? OR -  
BEHAVIOUR?)

S22 1 S20 AND S18

S23 37 S20 AND S17

S24 19 S21 AND S18

S25 64 S2:S4 AND S6:S7

S26 0 S25 AND S16

S27 47 S25 AND (CONDITION? ? OR MODE OR MODES OR STATE OR STATES -  
OR CIRCUMSTANCE? OR SITUATION? ? OR PERFORM? OR BEHAVIOR? OR -  
BEHAVIOUR?)

S28 1 S27 AND S18

S29 56 S22:S24 OR S28

S30 8 S29/2002:2005

S31 48 S29 NOT S30

S32 44 RD (unique items)

32/7/28 (Item 1 from file: 35)

DIALOG(R)File 35:Dissertation Abs Online

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01808120 ORDER NO: AADAA-I9937164

**Development and analysis of weak memory consistency models to  
accelerate shared-memory multiprocessor systems**

Author: Yoon, Myungchul

Degree: Ph.D.

Year: 1998

Corporate Source/Institution: The University of Texas at Austin (0227)

Supervisor: Craig Chase

Source: VOLUME 60/07-B OF DISSERTATION ABSTRACTS INTERNATIONAL.

PAGE 3480. 124 PAGES

ISBN: 0-599-38478-6

This dissertation develops a new memory consistency model for shared memory parallel computers. Previous memory consistency models overconstrained the parallelism available in a program. For example, Release Consistency, a well-known memory consistency model, stalls load and store instructions until all previous acquire operations have completed. In the new model, called Address Consistency, an acquire is allowed to execute as soon as the object it accesses is unlocked. The **status** of unrelated locks does not impact the scheduling of a memory operation. Address consistency requires that each lock in the system have an Associated **Memory Region** (ASM). The ASM for some lock *x* is the set of memory locations to which access is controlled by *x*. Although there is a unique ASM for each lock, Address Consistency permits a single memory location to be in the ASMs for two or

Updated - 09/89/339  
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S1	621075	PROCESS?R? ? OR CPU OR CPUS OR MICROPROCESS? OR CENTRAL()P-ROCESS??? ? OR MPU OR MPUS OR LOGIC(1W)CHIP? ?
S2	13695	S1(3N) (SURVEILL? OR SURVEY? OR MONITOR? OR CHECK??? ? OR TEST OR TESTS OR TESTED OR TESTING? ? OR DIAGNOS? OR DX OR DETECT???? ? OR ASSESS?)
S3	4184	S1(3N) (AUDIT OR AUDITS OR AUDITED OR AUDITING OR PROBE? ? - OR PROBING OR ANALYS? OR ANALYT? OR ANALYZ? OR SELFDIAGNOS? OR SELFTEST? OR SELFCHK?)
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S5	714051	PARTITION? OR OS OR OSS OR OPERATING(1W)SYSTEM? ? OR SYSTEM? ?(1W) (SOFTWARE? OR SOFT()WARE? ?)
S6	18600	(MULTIPLE OR MULTIPLES OR PLURALIT? OR MANY OR MULTI OR SEVERAL OR NUMEROUS OR ADDITIONAL OR DIFFERENT OR MULTITUD? OR - SECOND) (1W)S5
S7	17453	(BOTH OR BETWEEN OR VARIOUS OR VARIETY OR GROUP? ? OR NUMBER? ? OR PAIR? ? OR ANOTHER OR TWO OR THREE OR COUPLE) (1W)S5
S8	928510	MEMORY? OR MEMORIES OR NVRAM?
S9	1605	(NONVOLATILE OR NON()VOLATILE OR NV OR FLASH OR CORE OR BUBBLE OR MAGNET?? ? OR NONERAS? OR NON() (ERASE? OR ERASA?) OR -

PERMANENT) (1W) (RAM OR RAMS)

S10 1173675 NVM? ? OR E2PROM? OR EEPROM? OR EAROM? OR EPROM? ? OR EAPROM? OR ROM OR ROMS OR PROM OR PROMS OR FEPROM? ? OR NVS OR BOOTPROM? OR IDPROM? OR STORAGE?

S11 29059 S8:S10(3N) (PARTITION? OR DIVID? OR DIVISION? OR PORTION? OR REGION? OR SECTOR? ? OR ZONE? ? OR SECTION? OR SUBDIVID? OR - SUBDIVIS? OR SLICE?)

S12 90142 S8:S10(3N) (BLOCK? ? OR PART? ? OR PIECE? ? OR SUBSET? OR SUB( )SET? ? OR SEGMENT? ? OR AREA? ?)

S13 38270 S8:S10(3N) (SURVEILL? OR SURVEY? OR MONITOR? OR CHECK??? ? - OR TEST OR TESTS OR TESTED OR TESTING? ? OR DIAGNOS? OR DX OR DETECT???? ? OR ASSESS?)

S14 15462 S8:S10(3N) (AUDIT OR AUDITS OR AUDITED OR AUDITING OR PROBE? ? OR PROBING OR ANALYS? OR ANALYT? OR ANALYZ? OR SELFDIAGNOS? OR SELFTEST? OR SELFCHK?)

S15 11845 S8:S10(3N) (AUTODIAGNOS? OR AUTOTEST? OR AUTOCHK? OR BIST OR EXAMIN? OR EVALUAT? OR INSPECT? OR TRACK? OR TROUBLESHOOT? OR TROUBLE( )SHOOT?)

S16 1388503 STATUS?

S17 3593 S11:S12(S)S13:S15

S18 0 S20(S)S16

S19 40 S17(S)S16

S20 1102 S17(S) (CONDITION? ? OR MODE OR MODES OR STATE OR STATES OR CIRCUMSTANCE? OR SITUATION? ? OR PERFORM? OR BEHAVIOR? OR BEHAVIOUR?)

S21 17 S19(S) (TIME OR TIMES OR MINUTE? ? OR PERIOD? ? OR HOUR? ? - OR TIMEFRAME? OR TEMPORAL OR INTERVAL? ? OR FRAME OR FRAMES OR DURATION?)

S22 281291 (S1 OR S5 OR S8:S10) (10N) (CONDITION? ? OR MODE OR MODES OR STATE OR STATES OR CIRCUMSTANCE? OR SITUATION? ? OR PERFORM? - OR BEHAVIOR? OR BEHAVIOUR?)

S23 683 S17(S)S22

S24 194 S23(S) (TIME OR TIMES OR MINUTE? ? OR PERIOD? ? OR HOUR? ? - OR TIMEFRAME? OR TEMPORAL OR INTERVAL? ? OR FRAME OR FRAMES OR DURATION?)

S25 12187484 ELAPS? OR OVERDUE? OR DUE OR EXPIR? OR SPAN OR SPANS OR PASS?? ? OR PASSING OR PASSAGE? OR LAPS??? ? OR PAST OR CYCLE? ?

S26 581958 S25(3N) (TIME OR TIMES OR MINUTE? ? OR PERIOD? ? OR HOUR? ? OR TIMEFRAME? OR TEMPORAL OR INTERVAL? ? OR FRAME OR FRAMES OR DURATION?)

S27 3 S23(S)S26

S28 9 S20(S)S26

S29 26 S21 OR S27:S28

S30 15 S29/2002:2005

S31 11 S29 NOT S30

S32 10 RD (unique items)

S33 51 S2:S4(S)S6:S7

S34 1 S33(S)S16

S35 23 S33(S) (CONDITION? ? OR MODE OR MODES OR STATE OR STATES OR CIRCUMSTANCE? OR SITUATION? ? OR PERFORM? OR BEHAVIOR? OR BEHAVIOUR?)

S36 0 S35(3N) (TIME OR TIMES OR MINUTE? ? OR PERIOD? ? OR HOUR? ? OR TIMEFRAME? OR TEMPORAL OR INTERVAL? ? OR FRAME OR FRAMES OR DURATION?)

S37 1 S34 NOT S29

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S2	37144	S1(3N) (SURVEILL? OR SURVEY? OR MONITOR? OR CHECK??? ? OR TEST OR TESTS OR TESTED OR TESTING? ? OR DIAGNOS? OR DX OR DETECT???? ? OR ASSESS?)
S3	9056	S1(3N) (AUDIT OR AUDITS OR AUDITED OR AUDITING OR PROBE? ? - OR PROBING OR ANALYS? OR ANALYT? OR ANALYZ? OR SELFDIAGNOS? OR SELFTEST? OR SELFCHECK?)
S4	8990	S1(3N) (AUTODIAGNOS? OR AUTOTEST? OR AUTOCHECK? OR BIST OR - EXAMIN? OR EVALUAT? OR INSPECT? OR TRACK? OR TROUBLESHOOT? OR TROUBLE()SHOOT?)
S5	1355529	PARTITION? OR OS OR OSS OR OPERATING(1W)SYSTEM? ? OR SYSTEM? ?(1W) (SOFTWARE? OR SOFT()WARE? ?)
S6	37763	(MULTIPLE OR MULTIPLES OR PLURALIT? OR MANY OR MULTI OR SEVERAL OR NUMEROUS OR ADDITIONAL OR DIFFERENT OR MULTITUD? OR - SECOND) (1W)S5
S7	35506	(BOTH OR BETWEEN OR VARIOUS OR VARIETY OR GROUP? ? OR NUMBER? ? OR PAIR? ? OR ANOTHER OR TWO OR THREE OR COUPLE) (1W)S5
S8	1018576	MEMORY? OR MEMORIES OR NVRAM?
S9	4268	(NONVOLATILE OR NON()VOLATILE OR NV OR FLASH OR CORE OR BUBBLE OR MAGNET??? OR NONERAS? OR NON() (ERASE? OR ERASA?) OR - PERMANENT) (1W) (RAM OR RAMS)
S10	1727421	NVM? ? OR E2PROM? OR EEPROM? OR EAROM? OR EPROM? ? OR EAPROM? OR ROM OR ROMS OR PROM OR PROMS OR FPROM? ? OR NVS OR BOOTPROM? OR IDPROM? OR STORAGE?
S11	46057	S8:S10(3N) (PARTITION? OR DIVID? OR DIVISION? OR PORTION? OR REGION? OR SECTOR? ? OR ZONE? ? OR SECTION? OR SUBDIVID? OR - SUBDIVIS? OR SLICE?)
S12	133563	S8:S10(3N) (BLOCK? ? OR PART? ? OR PIECE? ? OR SUBSET? OR SUB()SET? ? OR SEGMENT? ? OR AREA? ?)
S13	69421	S8:S10(3N) (SURVEILL? OR SURVEY? OR MONITOR? OR CHECK??? ? - OR TEST OR TESTS OR TESTED OR TESTING? ? OR DIAGNOS? OR DX OR DETECT???? ? OR ASSESS?)
S14	22915	S8:S10(3N) (AUDIT OR AUDITS OR AUDITED OR AUDITING OR PROBE? ? OR PROBING OR ANALYS? OR ANALYT? OR ANALYZ? OR SELFDIAGNOS? OR SELFTEST? OR SELFCHECK?)
S15	21704	S8:S10(3N) (AUTODIAGNOS? OR AUTOTEST? OR AUTOCHECK? OR BIST

OR EXAMIN? OR EVALUAT? OR INSPECT? OR TRACK? OR TROUBLESHOOT?  
OR TROUBLE()SHOOT?)

S16 1028379 STATUS?

S17 15653885 TIME OR TIMES OR MINUTE? ? OR PERIOD? ? OR HOUR? ? OR TIME-  
FRAME? OR TEMPORAL OR INTERVAL? ? OR FRAME OR FRAMES OR DURAT-  
ION?

S18 435221 S17(3N)(ELAPS? OR OVERDUE? OR DUE OR EXPIR? OR SPAN OR SPA-  
NS OR PASS?? ? OR PASSING OR PASSAGE? OR LAPS??? ? OR PAST OR  
CYCLE? ?)

S19 5992 S11:S12(S)S13:S15

S20 47 S19(S)S16

S21 0 S20(S)S18

S22 12 S20(S)S17

S23 508678 (S1 OR S5 OR S8:S10)(10N)(S16 OR CONDITION? ? OR MODE OR M-  
ODES OR STATE OR STATES OR CIRCUMSTANCE? OR SITUATION? ? OR P-  
ERFORM? OR BEHAVIOR? OR BEHAVIOUR?)

S24 1015 S19(S)S23

S25 6 S24(S)S18

S26 141 S2:S4(S)S6:S7

S27 0 S26(S)S22

S28 1 S26(S)S16

S29 58 S26(S)(CONDITION? ? OR MODE OR MODES OR STATE OR STATES OR  
CIRCUMSTANCE? OR SITUATION? ? OR PERFORM? OR BEHAVIOR? OR BEH-  
AVIOUR?)

S30 17 S29(S)S17

S31 35 S22 OR S25 OR S28 OR S30

S32 2 S31/2002:2005

S33 33 S31 NOT S32

S34 17 RD (unique items)

34/3,K/12 (Item 1 from file: 160)  
DIALOG(R)File 160:Gale Group PROMT(R)  
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01852600  
CMOS SYNCHRONOUS RAM MODULE INTRODUCED BY IDT  
News Release December 17, 1987 p. 1

... x 8, the IDT7MP6025 comes in three speed grades: 45, 55 and 70  
nanosecond (ns) **cycle times**. The fastest part, 45 ns, can therefore be  
operated at a 22 megahertz (MHz) clock...

... to achieve using discrete memory components. With the IDT7MP6025,  
designers can now purchase a fully- **tested high-performance memory**  
**block** complete with edge-triggered address registers; registered separate  
I/O data lines and registered write...

... logic chips for these types of modules which optimized the required  
control functions for increased **performance**. One of these **logic chips**  
combines address registers and decoders and also provides a delay for the  
write enable input...  
? t34/3,k/13-14

34/3,K/13 (Item 1 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
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02000719 SUPPLIER NUMBER: 18768623 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
ALR's Revolution Quad6 Server. (Advanced Logic Research Pentium-Pro SMP  
system) (Hardware Review) (Evaluation)



Updated S- 09/891,339  
Applicants S.

File 347:JAPIO Nov 1976-2004/Aug(Updated 041203)  
(c) 2004 JPO & JAPIO  
File 350:Derwent WPIX 1963-2005/UD,UM &UP=200504  
(c) 2005 Thomson Derwent  
File 348:EUROPEAN PATENTS 1978-2005/Jan W03  
(c) 2005 European Patent Office  
File 349:PCT Fulltext 1979-2002/UB=20050120,UT=20050113  
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File 324:German National Patents 1980-2004/Nov  
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Set	Items	Description
S1	12	AU=AUSTEN C?
S2	212	AU=OLIVER D?
S3	216	S1:S2
S4	826633	PARTITION? OR OS OR OSS OR OPERATING(1W)SYSTEM? ?
S5	9903	SYSTEM? ?() (SOFTWARE? OR SOFT()WARE? ?)
S6	15	S3 AND S4:S5

6/9/1 (Item 1 from file: 347)  
DIALOG(R)File 347:JAPIO  
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08107823 \*\*Image available\*\*  
METHOD AND DEVICE FOR REPORTING ERROR LOG IN LOGICAL ENVIRONMENT

PUB. NO.: 2004-220582 [JP 2004220582 A]  
PUBLISHED: August 05, 2004 (20040805)  
INVENTOR(s): AUSTEN CHRISTOPHER HARRY  
KITAMORN ALONGKORN  
OLIVER DOUGLAS WAYNE  
WILLOUGHBY DAVID R  
APPLICANT(s): INTERNATL BUSINESS MACH CORP (IBM)  
APPL. NO.: 2003-421702 [JP 2003421702]  
FILED: December 18, 2003 (20031218)  
PRIORITY: 03 339770 [US 2003339770], US (United States of America),  
January 09, 2003 (20030109)  
INTL CLASS: G06F-011/30; G06F-009/46; G06F-011/34

#### ABSTRACT

PROBLEM TO BE SOLVED: To provide an improved method, a device and a computer instruction for reporting an error log in an LPAR (logical **partitioning**) data processing system.  
SOLUTION: This invention provides the method, the device and the computer instruction for managing the error log. A request is received from a **partition** within a plurality of **partitions** in the LPAR data processing system to access error information. Whether the error information is present in a buffer or not is determined. In response to that the error information is not present in the buffer, the error information is searched from a nonvolatile memory. The obtained error information searched from the nonvolatile memory is stored in the buffer. The error information is stored in the buffer only for a prescribed period of time. After the period of time, the error information is deleted or removed from the buffer. The outdated error information is not returned to the plurality of **partitions**.

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